

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 06-338775

(43)Date of publication of application : 06.12.1994

(51)Int.Cl.

H03K 17/62

(21)Application number : 05-152851

(71)Applicant : SONY CORP

(22)Date of filing : 31.05.1993

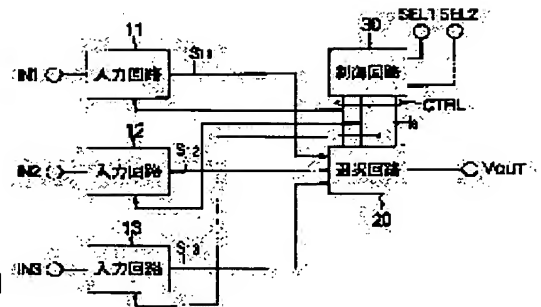
(72)Inventor : CHIBA HIDEKI
YOSHIZAWA YUKIE

(54) SWITCHING CIRCUIT

(57)Abstract:

PURPOSE: To provide a switching circuit applicable to the case to handle large amplitude and a circuit with large difference in a power supply voltage, etc., and capable of surely reducing crosstalk.

CONSTITUTION: The crosstalk can be reduced by comprising the switching circuit in two stages, passing input signals IN1-IN3 when they are selected by control signals CTRL1-CTRL3 by a control circuit 30 and setting a mute state by connecting the output to a constant voltage source V when they are not selected an input circuits 11-13 as the switching circuit at the initial stage, and obtaining the final output signal VOUT by selecting the output of the input circuits 11-30 based on the control signals CTRL1-CTRL3 by the control circuit 30 at a selection circuit 20 as the switching circuit at the next stage.



LEGAL STATUS

[Date of request for examination]

17.12.1999

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

3271211

[Date of registration]

25.01.2002

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office